

NetApp Verified Architecture

# NetApp ONTAP AI with NVIDIA DGX A100 Systems NVA Design

David Arnette and Sung-Han Lin, NetApp October 2020 | NVA-1151-DESIGN

## Abstract

This document describes a NetApp Verified Architecture for machine learning and artificial intelligence workloads using NetApp<sup>®</sup> AFF A800 storage systems, NVIDIA DGX<sup>™</sup> A100 systems, and NVIDIA<sup>®</sup> Mellanox<sup>®</sup> network switches. It also includes benchmark test results for the architecture as implemented.

In partnership with



## TABLE OF CONTENTS

Executive Summary	
Program Summary	4
NetApp ONTAP AI Solution	4
Deep Learning Data Pipeline	5
Solution Overview	7
NVIDIA DGX A100 Systems	9
NVIDIA NGC	9
NetApp AFF Systems	9
NetApp ONTAP 9	9
NetApp FlexGroup Volumes	10
NetApp Trident	11
NVIDIA Mellanox Networking	11
Technology Requirements	
Hardware Requirements	12
Software Requirements	12
Solution Architecture	
Network Topology and Switch Configuration	13
Storage System Configuration	15
Host Configuration	17
Solution Verification	
Infrastructure Validation	
Deep Learning Workload Validation	20
Solution Sizing Guidance	21
Conclusion	
Acknowledgments	
Where to Find Additional Information	
NetApp AFF Systems	
NetApp Interoperability Matrix	
NetApp Trident	
NVIDIA DGX A100 Systems	
NVIDIA Mellanox Networking	
Machine Learning Frameworks	23

Dataset and Benchmarks	
Version History	

## LIST OF TABLES

Table 1) Hardware requirements	12
Table 2) Software requirements.	12

## LIST OF FIGURES

Figure 1) NetApp ONTAP AI family with NVIDIA DGX A100 systems	5
Figure 2) Components of the edge-core-cloud data pipeline	6
Figure 3) NetApp ONTAP AI verified architecture.	8
Figure 4) Network switch port configuration.	14
Figure 5) VLAN connectivity for DGX A100 and storage system ports	15
Figure 6) Storage system configuration	16
Figure 7) Network port and VLAN configuration of the DGX A100 systems.	17
Figure 8) NCCL bandwidth test result	19
Figure 9) FIO bandwidth test results (MiB/s)	20
Figure 10) FIO IOPS test results (operations/sec)	20
Figure 12) MLPerf v0.7 average images per second	21

# **Executive Summary**

This document contains validation information for the NetApp<sup>®</sup> ONTAP<sup>®</sup> AI reference architecture for machine learning (ML) and artificial intelligence (AI) workloads. This design was implemented using a <u>NetApp AFF A800 all-flash storage system</u>, four DGX A100 systems, and NVIDIA Mellanox Spectrum Ethernet and Quantum InfiniBand switches. The operation and performance of this system was validated using industry-standard benchmark tools. Based on the validation testing results, this architecture delivers excellent training and inferencing performance. Customers can also easily and independently scale compute and storage resources from half-rack to multi-rack configurations with predictable performance to meet any machine learning workload requirement.

# **Program Summary**

The NetApp Verified Architecture program provides customers with reference configurations and sizing guidance for specific workloads and use cases. These solutions are:

- Thoroughly tested
- Designed to minimize deployment risks
- Designed to accelerate time to market

This document is for NetApp and partner solutions engineers and customer strategic decision makers. It describes the architecture design considerations that were used to determine the specific equipment, cabling, and configurations required to support the validated workload.

## **NetApp ONTAP AI Solution**

The NetApp ONTAP AI reference architecture, powered by DGX A100 systems and NetApp cloudconnected storage systems, was developed and verified by NetApp and NVIDIA. It gives IT organizations an architecture that:

- Eliminates design complexities
- Allows independent scaling of compute and storage
- Enables customers to start small and scale seamlessly
- Offers a range of storage options for various performance and cost points

NetApp ONTAP AI tightly integrates DGX A100 systems and NetApp AFF A800 storage systems with state-of-the-art networking. NetApp ONTAP AI simplifies artificial intelligence deployments by eliminating design complexity and guesswork. Customers can start small and grow nondisruptively while intelligently managing data from the edge to the core to the cloud and back.

Figure 1 shows several variations in the ONTAP AI family of solutions with DGX A100 systems. The AFF A800 system performance has currently been verified with up to four DGX A100 systems. By adding storage controller pairs to the ONTAP cluster, the architecture can scale to multiple racks to support many DGX A100 systems and petabytes of storage capacity with linear performance. This approach offers the flexibility to alter compute-to-storage ratios independently based on the size of the data lake, the deep learning (DL) models that are used, and the required performance metrics.

Figure 1) NetApp ONTAP AI family with NVIDIA DGX A100 systems.



The number of DGX A100 systems and AFF systems per rack depends on the power and cooling specifications of the rack in use. Final placement of the systems is subject to computational fluid dynamics analysis, airflow management, and data center design.

# **Deep Learning Data Pipeline**

Deep learning is the engine that enables businesses to detect fraud, improve customer relationships, optimize supply chains, and deliver innovative products and services in an increasingly competitive marketplace. The performance and accuracy of DL models are significantly improved by increasing the size and complexity of the neural network as well as the amount and quality of data that is used to train the models.

Given the massive datasets required, it is crucial to architect an infrastructure that offers the flexibility to deploy across environments. At a high level, an end-to-end DL deployment consists of three phases through which the data travels: the edge (data ingest and inferencing), the core (training clusters and a data lake), and the cloud (archive, tiering, and dev/test). This is typical of applications such as the Internet of Things (IoT) for which data spans all three realms of the data pipeline. Figure 2 presents an overview of the components in each of the three realms.

Ingest Data prep Training & Deployment Analysis & Tiering Data prep Data prep Data prep Data collection Data prep Data prep Data prep Data prep Data prep Data collection Data prep Data prep Data prep Data prep Data collection Data prep Data prep Data prep Data collection Data prep Data collection Data prep Data prep Data prep Data collection Data prep Data collection Data prep Data collection Data prep Data prep Data prep Data collection Data collection Data collection Data collection Data prep Data collection Data coll	Edge	( O Core		Cloud
Image: Control of the second secon	Ingest	Data mean	Deployment	Analysis & Tiering
Image: Construction  Image: Construction    Ima		2 3		aws
Unified data lake Test Repo	010001010			Microsoft Azure
Data collection  Accregation  Fxploration  Deployment  Cloud AI (GPU instances)	•			Google Cloud
Edge-level AI  Normalization - Training Model serving - Data tiering	Data collection  Edge-level Al	Aggregation - Exploration  Normalization - Training		
		s for AI FlexPOD AI ONTAP AI	NetApp Al	Colos StorageGRID Cloud Volu

Figure 2) Components of the edge-core-cloud data pipeline.

The following list describes some of the activities that occur in one or more of these areas.

- **Ingest.** Data ingestion usually occurs at the edge; for example, by capturing data streaming from autonomous cars or point-of-sale devices. Depending on the use case, an IT infrastructure might be needed at or near the ingestion point. For instance, a retailer might need a small footprint in each store that consolidates data from multiple devices.
- **Data prep.** Preprocessing is necessary to normalize and cleanse the data before training. Preprocessing takes place in a data lake, possibly in the cloud, in the form of an Amazon S3 tier or in on-premises storage systems such as a file store or an object store.
- **Training and validation.** For the critical training phase of DL, data is typically copied from the data lake into the training cluster at regular intervals. The servers that are used in this phase use GPUs to parallelize computations, creating a tremendous appetite for data. Meeting the raw I/O bandwidth needs is crucial for maintaining high GPU utilization.
- **Deployment.** The trained models are tested and deployed into production. Alternatively, they could be fed back to the data lake for further adjustments of input weights; or in IoT applications the models could be deployed to the smart edge devices.
- Analysis and tiering. New cloud-based tools become available at a rapid pace, so additional analysis or development work might be conducted in the cloud. Cold data from past iterations might be saved indefinitely. Many AI teams prefer to archive cold data to object storage in either a private or a public cloud. Based on compute requirements, some applications work well with object storage as the primary data tier.

Depending on the application, DL models work with large amounts of structured and unstructured data. This difference imposes a varied set of requirements on the underlying storage system, both in terms of size of the data that is being stored and the number of files in the dataset.

High-level storage requirements include:

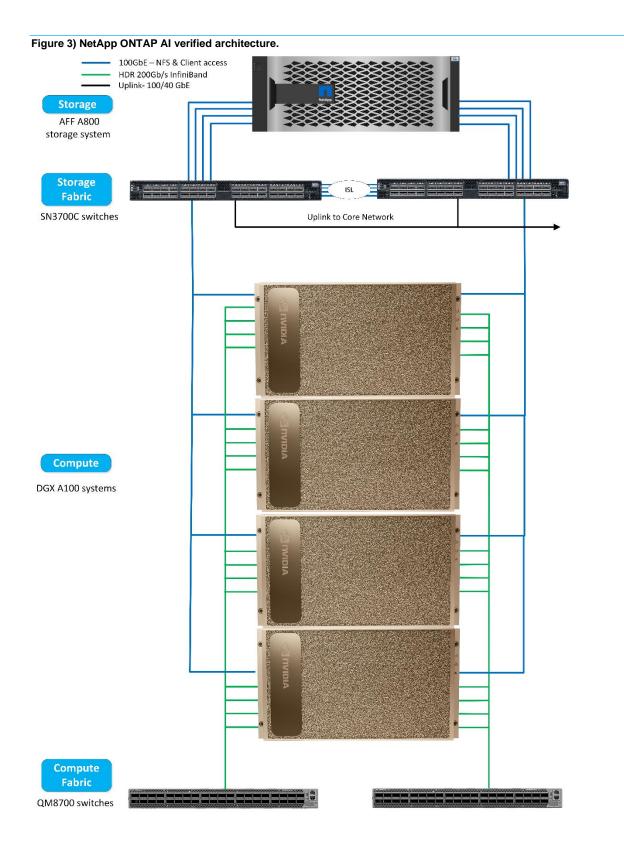
- The ability to store and retrieve millions of files concurrently
- Storage and retrieval of diverse data objects such as images, audio, video, and time-series data
- Delivery of high parallel performance at low latencies to meet the GPU processing speeds
- Seamless data management and data services that span the edge, the core, and the cloud

Combined with superior cloud integration and the software-defined capabilities of NetApp ONTAP, NetApp AFF systems support a full range of data pipelines that spans the edge, the core, and the cloud for DL. This document focuses on solutions for the training and inference components of the data pipeline.

# **Solution Overview**

Deep learning systems leverage algorithms that are computationally intensive and that are uniquely suited to the architecture of GPUs. Computations that are performed in DL algorithms involve an immense volume of matrix multiplications running in parallel. Advances in individual and clustered GPU computing architectures leveraging DGX systems have made them the preferred platform for workloads such as high-performance computing (HPC), DL, video processing, and analytics. Maximizing performance in these environments requires a supporting infrastructure, including storage and networking, that can keep GPUs fed with data. Dataset access must therefore be provided at ultra-low latencies with high bandwidth.

This reference architecture was validated with one NetApp AFF A800 system, four DGX A100 systems, two NVIDIA Mellanox Quantum<sup>™</sup> QM8700 InfiniBand (IB) switches, and two NVIDIA Mellanox Spectrum<sup>™</sup> SN3700 100GbE switches. Figure 3 shows the basic solution architecture.



## **NVIDIA DGX A100 Systems**

The DGX A100 system is a fully integrated, turnkey hardware and software system that is purpose-built for DL workflows. Each DGX A100 system is powered by eight NVIDIA A100 GPUs that are configured in a hybrid cube-mesh topology that uses NVIDIA NVLink<sup>®</sup> and NVSwitch<sup>®</sup> technology. This configuration provides an ultra-high bandwidth, low-latency fabric for inter-GPU communication within the DGX A100 system. This topology is essential for multi-GPU training, eliminating the bottleneck that is associated with PCIe-based interconnects that cannot deliver linearity of performance as GPU count increases. The DGX A100 system is also equipped with high-bandwidth, low-latency network interconnects for multinode clustering over RDMA-capable fabrics such as Ethernet (RoCE) and InfiniBand.

## **NVIDIA NGC**

The DGX A100 system leverages <u>NVIDIA NGC<sup>™</sup></u>, a cloud-based container registry for GPU-accelerated software. NGC provides containers for today's most popular DL frameworks such as Caffe2, TensorFlow, PyTorch, MXNet, and TensorRT<sup>™</sup>, which are optimized for NVIDIA GPUs. The containers integrate the framework or application, necessary drivers, libraries, and communications primitives, and they are optimized across the stack by NVIDIA for maximum GPU-accelerated performance. NGC containers incorporate the NVIDIA CUDA<sup>®</sup> Toolkit, which provides the CUDA Basic Linear Algebra Subroutines Library (cuBLAS), the CUDA Deep Neural Network Library (cuDNN), and much more. The NGC containers also include the NVIDIA Collective Communications Library (NCCL) for multi-GPU and multinode collective communication primitives, enabling topology-awareness for DL training. NCCL enables communication between GPUs inside a single DGX A100 system and across multiple DGX A100 systems.

## NetApp AFF Systems

NetApp AFF state-of-the-art storage systems enable IT departments to meet enterprise storage requirements with industry-leading performance, superior flexibility, cloud integration, and best-in-class data management. Designed specifically for flash, AFF systems help accelerate, manage, and protect business-critical data.

The NetApp AFF A800 system is the industry's first end-to-end NVMe solution. For NAS workloads, a single AFF A800 system supports throughput of 25GBps for sequential reads and one million IOPS for small random reads at sub-500 µs latencies.

AFF A800 systems support the following features:

- Massive throughput of up to 300GBps and 11.4 million IOPS in a 24-node cluster
- 100GbE and 32Gb FC connectivity
- Up to 30TB solid-state drives (SSDs) with multistream write
- High density with 2PB in a 2U drive shelf
- Scaling from 200TB (2 controllers) to 9.6PB (24 controllers)
- NetApp ONTAP 9.7 includes a complete suite of data protection and replication features for industryleading data management

Other NetApp storage systems, such as the AFF A700, AFF A400, and AFF A220, offer lower performance and capacity options for smaller deployments at lower cost points.

## **NetApp ONTAP 9**

NetApp ONTAP 9, the latest generation of storage management software from NetApp, enables businesses to modernize infrastructure and transition to a cloud-ready data center. Leveraging industry-leading data management capabilities, ONTAP enables the management and protection of data with a single set of tools, regardless of where that data resides. Data can also be moved freely to wherever it's needed—the edge, the core, or the cloud. ONTAP 9 includes numerous features that simplify data

management, accelerate and protect critical data, and enable next-generation infrastructure capabilities across hybrid cloud architectures.

## Simplify Data Management

Data management is crucial to enterprise IT operations so that appropriate resources are used for applications and for datasets. ONTAP includes the following features to streamline and simplify operations and reduce the total cost of operation:

- Inline data compaction, compression, and deduplication. Compression delivers the primary benefit for alpha-numeric data often used in ML/DL workloads. Data compaction reduces wasted space inside storage blocks, and deduplication significantly increases effective capacity.
- **Minimum, maximum, and adaptive quality of service (QoS).** Granular QoS controls help maintain performance levels for critical applications in highly shared environments and allows production and development to share infrastructure with guaranteed allocation of resources.
- ONTAP FabricPool. This feature provides automatic tiering of cold data to public and private cloud storage options, including Amazon Web Services (AWS), Microsoft Azure, and NetApp StorageGRID<sup>®</sup>. For more details on FabricPool, see <u>TR-4598</u>.

## Accelerate and Protect Data

ONTAP delivers superior levels of storage performance and data protection and extends these capabilities with:

- **Performance and lower latency.** ONTAP offers the highest possible throughput at the lowest possible latency.
- **Data protection.** ONTAP provides built-in data protection capabilities with common management across all platforms.
- **NetApp Volume Encryption.** ONTAP offers native volume-level encryption with both onboard and external key management support.
- Multitenancy and Multifactor authentication. ONTAP enables sharing of infrastructure resources with the highest levels of security.

## **Future-Proof Infrastructure**

ONTAP 9 helps meet demanding and constantly changing business needs.

- Seamless scaling and nondisruptive operations. ONTAP supports the nondisruptive addition of capacity to existing controllers as well as to scale-out clusters. Customers can upgrade to the latest technologies such as NVMe and 32Gb FC without costly data migrations or outages.
- **Cloud connection.** ONTAP is the most cloud-connected storage management software, with options for software-defined storage (ONTAP Select) and cloud-native instances (NetApp Cloud Volumes Service) in all public clouds.
- Integration with emerging applications. ONTAP offers enterprise-grade data services for nextgeneration platforms and applications using the same infrastructure that supports existing enterprise apps.

## NetApp FlexGroup Volumes

The training dataset is usually a large collection of many, potentially billions of files. Files can include text, audio, video, and other forms of unstructured data that must be stored and processed to be read in parallel. The storage system must store many small files and must read those files in parallel for sequential and random I/O.

A NetApp FlexGroup ONTAP volume, as shown in Figure 6, is a single namespace that is made up of multiple constituent member volumes and that is managed and acts like a NetApp FlexVol<sup>®</sup> volume to

storage administrators. Files in a FlexGroup volume are allocated to individual member volumes and are not striped across volumes or nodes. They enable the following capabilities:

- FlexGroup volumes enable up to 20PB of capacity and predictable low latency for high-metadata workloads.
- They support up to 400 billion files in the same namespace.
- They support parallelized operations in NAS workloads across CPUs, nodes, aggregates, and constituent FlexVol volumes.

## **NetApp Trident**

<u>Trident</u> from NetApp is an open-source dynamic storage provisioner for Docker and Kubernetes. Combined with NGC and popular orchestrators such as Kubernetes and Docker Swarm, Trident enables customers to seamlessly deploy DL NGC container images onto NetApp storage, which provides an enterprise-grade experience for AI container deployments. These deployments include automated orchestration, cloning for testing and development, upgraded testing that uses cloning, protection and compliance copies, and many more data management use cases for the NGC AI and DL container images.

## **NVIDIA Mellanox Networking**

# NVIDIA Mellanox Spectrum Switches—The Right Choice for Deep Learning Workloads

Networking is a critical part of the DL infrastructure that is responsible for moving massive amounts of data between the end points efficiently and effectively. Spectrum Ethernet switches with consistent performance, intelligent load balancing and comprehensive telemetry are an ideal network element for DL workloads.

#### **Consistent Performance**

Spectrum Ethernet switches provide a high bandwidth and consistently low latency data path for GPU-GPU and GPU-Storage communications. Spectrum along with NVIDIA Mellanox ConnectX<sup>®</sup> adapters inside the DGX A100 systems implement a tight and efficient ECN (Explicit Congestion Notification) mechanism that mitigates transient congestion and smooths traffic bursts to maximize network goodput.

#### Intelligent Load Balancing

The network is a shared resource and its bandwidth needs to be shared in a fair manner across different flows and across different endpoints. Packet buffer architecture is one of the foundational attributes of the switch that impacts performance as well as traffic fairness. The Spectrum switches feature a flexible and fully shared buffer architecture that ensures fair and balanced performance across all ports even when using a mix of different port speeds. Many high-speed switches in the market use fragmented packet buffers. Switches with fragmented buffers have scheduling issues and can preferentially give more bandwidth to certain ports/flows and at the cost of others. This traffic imbalance leads to more performance variation and in turn hamper distributed DL performance.

#### **Comprehensive Telemetry**

To reap high return on investment from the DL infrastructure, uptimes must be improved, and the network must be proactively monitored. Traditional methods of centrally processing the telemetry data acquired via SNMP or streaming can quickly become prohibitively expensive at terabit speeds. NVIDIA Mellanox What Just Happened® (WJH) leverages silicon level capabilities to quickly identify and export granular information about issues as soon as they happen. Because this capability in built-in to the platform, only the data pertinent to the issue is gathered at the central data collector. WJH makes proactive monitoring

scalable and practical at terabit speeds. With WJH, customers can dramatically reduce mean time to issue resolution and plan capacity better.

## NVIDIA Mellanox Quantum 8700 HDR 200Gb InfiniBand Smart Switches

NVIDIA provides the world's smartest switches, enabling in-network computing through Scalable Hierarchical Aggregation and Reduction Protocol (SHARP)<sup>™</sup> technology. The NVIDIA Mellanox Quantum QM8700 series has the highest fabric performance available in the market with up to 16Tbps of nonblocking bandwidth with sub 130ns port-to-port latency. Built with the Mellanox Quantum InfiniBand switch device, the QM8700 series provides up to forty 200Gbps (or eighty 100Gbps) full bidirectional bandwidth per port.

QM8700 series enables efficient computing with features such as static routing, adaptive routing, congestion control, self-healing and enhanced VL mapping to enable modern topologies (non-blocking and blocking Fat Trees, Dragonfly+, Torus, and more). These ensure the most efficient fabric, maximizing customers' return on investments (ROI).

#### **In-network Computing**

The NVIDIA Mellanox Quantum switch improves the performance of collective operations by processing the data as it traverses the network, eliminating the need to send data multiple times between endpoints. It also supports the aggregation of large data vectors at wire speed to enable MPI large vector reduction operations, which are crucial for machine learning applications.

SHARP is readily exposed when using NCCL (the NVIDIA Collective Communication Library), so that any ML/DL framework can leverage it to scale-up its performance.

# **Technology Requirements**

This section covers the hardware and software that was used for all the testing described in Section 0, Solution Verification.

## **Hardware Requirements**

Table 1 lists the hardware components that were used to verify this solution.

Hardware	Quantity
DGX A100 systems	4
AFF A800 storage system	1 high-availability (HA) pair, includes 48x 1.92TB NVMe SSDs
SN3700C Ethernet switches	2
QM8700 InfiniBand switches	2

#### **Software Requirements**

Table 2 lists the software components that were used to validate the solution.

Software	Version
ONTAP storage OS	9.7P4
Onyx switch firmware	ONYX OS 3.9.0608
Quantum switch firmware	MLNX-OS 3.9.0606

#### Table 2) Software requirements.

Software	Version
DGX OS	4.99.9
Docker container platform	19.03.8
Container version	nvcr.io/nvidia/mxnet:20.06-py3 – Mlperf test tensorflow:20.05-tf2-py3 – other tests
OFED version	OFED-internal-5.0-2.1.8
NCCL test version	https://github.com/NVIDIA/nccl- tests/tree/ec1b5e22e618d342698fda659efdd5918da6bd9f
DALI test version	https://github.com/NVIDIA/DALI/tree/73b8dd39d54dfc6e38d7ffb243a4f9489e228484
FIO version	3.1

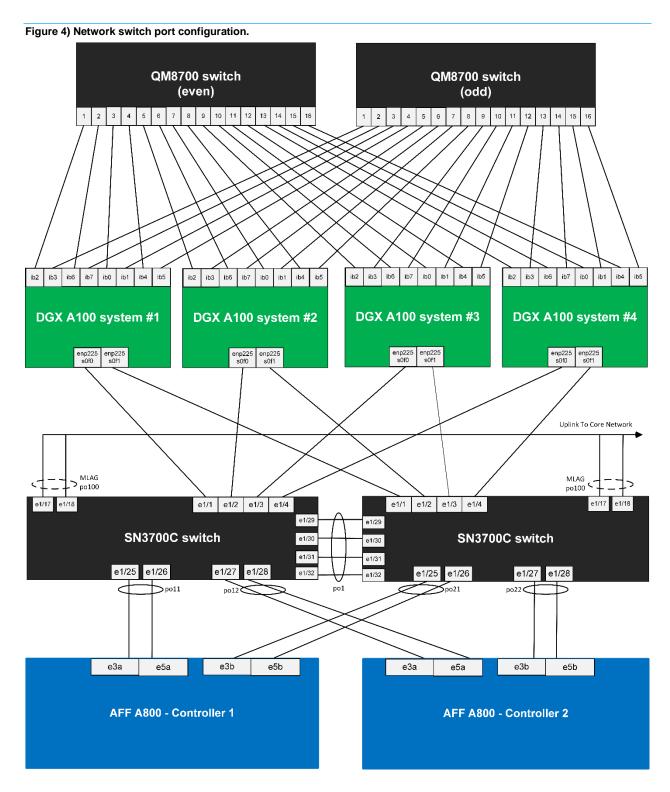
# **Solution Architecture**

This reference architecture has been verified to meet the requirements for running deep learning workloads. This ability enables data scientists to deploy DL frameworks and applications on a prevalidated infrastructure, helping to eliminate risks and allowing businesses to focus on gaining valuable insights from their data. This architecture can also deliver exceptional storage performance for other HPC workloads without any modification or tuning of the infrastructure.

## **Network Topology and Switch Configuration**

This reference architecture leverages separate fabrics for compute cluster interconnect and storage access. The compute cluster network uses a pair of QM8700 HDR InfiniBand switches operating as independent redundant fabrics. Each DGX A100 system is connected using eight single-ported ConnectX-6 IB cards, with even-numbered ports connected to one switch and odd-numbered ports connected to the other switch.

Two SN3700C 100Gb Ethernet switches are used to provide NFS storage connectivity as well as in-band management and client access to the DGX A100 systems. The SN3700C switches are configured for Multichassis Link Aggregation (MLAG) to allow aggregation of bandwidth and transparent failover in the event of a switch failure. A dual-ported ConnectX-6 card configured for Ethernet is used to provide one port from each DGX A100 system to each SN3700C switch. Also, each A800 storage system is connected using four ports from each controller, with a two-port LACP bond to each switch to ensure balanced workload distribution across the storage controllers. Figure 4 shows the overall network topology.



The Ethernet network is configured with multiple VLANs to isolate specific traffic types. NFS storage traffic, in-band management, and client access each have dedicated VLANs to ensure proper maximum transmission unit (MTU) and other settings for each traffic type. For example, NFS storage traffic requires an MTU of 9000, while other typical Ethernet traffic uses an MTU of 1500.

Figure 5 shows the VLAN connectivity for the hosts and storage system controllers.

Note: The AFF A800 storage system controllers have separate 1GbE management interfaces that are plugged into a separate management switch.

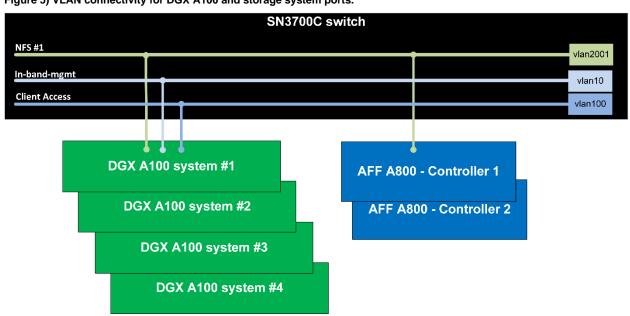
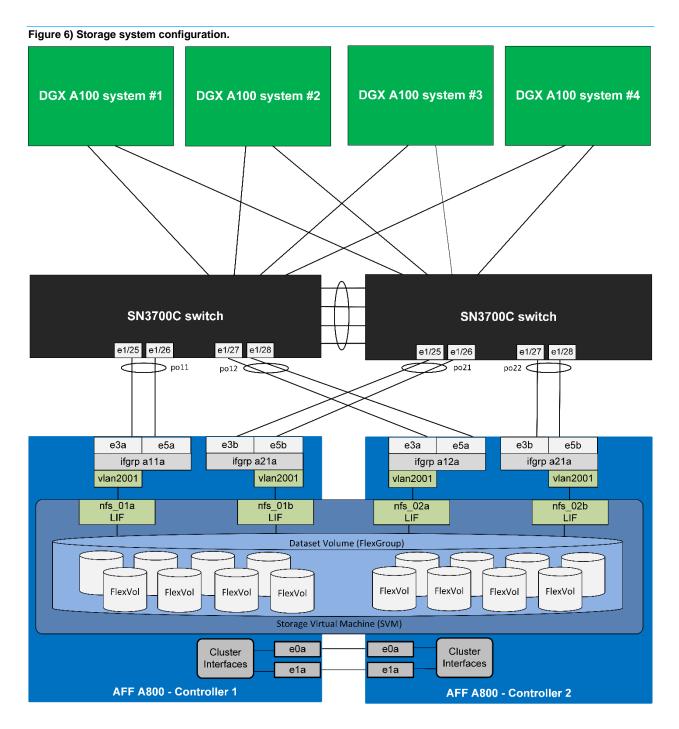


Figure 5) VLAN connectivity for DGX A100 and storage system ports.

## **Storage System Configuration**

To support the storage network requirements of any potential workload on this architecture, each storage controller is provisioned with four 100GbE ports in addition to the onboard ports that are required for storage cluster interconnection. Figure 6 shows the storage system configuration. Each controller is configured with a two-port LACP interface group (ifgrp in Figure 6) to each switch. These interface groups provide up to 200Gbps of resilient connectivity to each switch for data access. Two VLANs are provisioned for NFS storage access, and both storage VLANs are trunked from the switches to each of these interface groups. This configuration allows concurrent access from each host to the data through multiple interfaces, which improves the potential bandwidth that is available to each host.

All data access from the storage system is provided through NFS access from a storage virtual machine (SVM) that is dedicated to this workload. The SVM is configured with a total of four logical interfaces (LIFs), with two LIFs on each storage VLAN. Each interface group hosts a single LIF, resulting in one LIF per VLAN on each controller with a dedicated interface group for each VLAN. However, both VLANs are trunked to both interface groups on each controller. This configuration provides the means for each LIF to fail over to another interface group on the same controller, so that both controllers stay active in the event of a network failure.



For logical storage provisioning, the solution uses a FlexGroup volume to provide a single pool of storage that is distributed across the nodes in the storage cluster. Each controller hosts an aggregate of 46 disk partitions, with both controllers sharing every disk. When the FlexGroup is deployed on the data SVM, eight FlexVol volumes are provisioned on each aggregate and are then combined into the FlexGroup. This approach allows the storage system to provide a single pool of storage that can scale up to the maximum capacity of the array and provide exceptional performance by leveraging all the SSDs in the array concurrently. NFS clients can access the FlexGroup as a single mount point through any of the LIFs that are provisioned for the SVM. Capacity and client access bandwidth can be increased simply by adding more nodes to the storage cluster. Note that multiple IP addresses are not required to achieve full

performance of either the controllers or FlexGroup volume, but they do allow for better hashing and load distribution in the network.

## **Host Configuration**

For network connectivity, each DGX A100 system is provisioned with eight ConnectX-6 single-port network interface cards for compute cluster connectivity and one ConnectX-6 dual-port card for storage and client access connectivity. These cards support up to 200Gb link speeds for both InfiniBand and Ethernet. In this reference architecture the eight single-port cards are configured for IB and connected to a pair of QM8700 IB switches for compute cluster connectivity. The ports on the dual-ported card are connected to a pair of SN3700C Ethernet switches for storage and client networking. Figure 7 shows the network port and VLAN configuration of the DGX A100 systems.

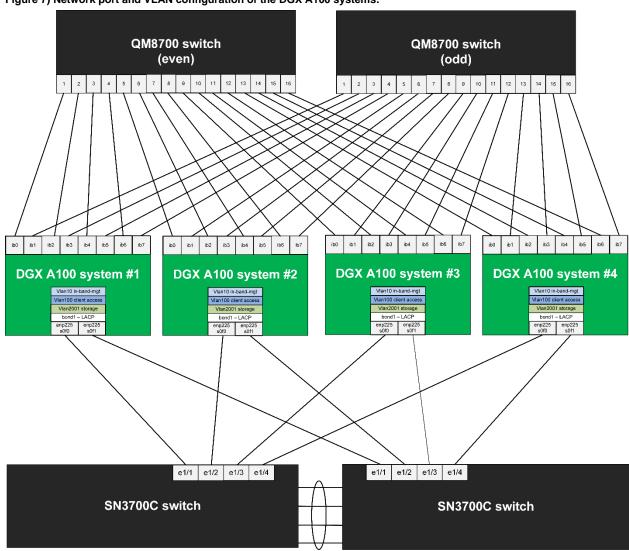


Figure 7) Network port and VLAN configuration of the DGX A100 systems.

For Ethernet storage and client access networking, the two physical ports are configured as an LACP port-channel on the host side and an MLAG on the switch side. Three VLANs are configured on this bond to isolate traffic for in-band management, external client access, and NFS storage traffic. Due to the high-performance capabilities of the AFF A800 storage system, host-side NFS filesystem caching was disabled for this testing.

DGX OS 4.99 and later use the Linux 5.3 kernel, which includes the NFS nConnect feature that significantly enhances NFSv3 storage performance. nConnect allows a single NFS mount to leverage multiple TCP sessions to increase the available bandwidth potentially up to wire-speed maximum. This architecture was validated with nConnect to simplify host configuration while delivering performance comparable to previous configurations with multiple mounts. Specific host-side mount parameters used in this testing are listed below:

- nConnect=8. Creates 8 TCP sessions for each mounted volume to improve overall performance.
- rsize=262144, wsize=262144. Sets the maximum read and write transfer size to 256k. ONTAP supports NFS transfer sizes as high as 1MB, but testing has shown that 256k delivers maximum throughput at the lowest latency.

# **Solution Verification**

This reference architecture was validated using synthetic benchmark utilities and deep learning benchmark tests to establish baseline performance and operation of the system. Each of the tests described in this section was performed with the specific equipment and software listed in section 5, Technology Requirements.

## Infrastructure Validation

The following tests were performed with one, two, and four DGX A100 systems to validate basic operation and performance of the deployed infrastructure:

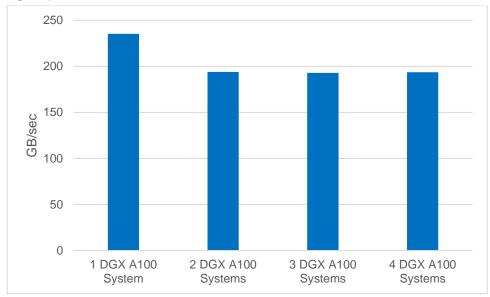
- NVIDIA nvsm stress test. This test suite performs a pass/fail verification of many crucial DGX A100 systems. All systems should report a passing status for the tests in this group.
- NVIDIA NCCL all\_reduce\_perf
- FIO bandwidth test
- FIO I/O operations per second (IOPS) test
- Metadata performance testing with mdtest

The following sections describe details and results for each of these tests.

## NVIDIA NCCL all\_reduce\_perf Test

This test validates the performance of the interconnects between GPUs. For single-node systems the bottleneck should be the NVIDIA NVLink connection between GPUs. For multi-node systems the bottleneck should be the ethernet or InfiniBand connections between DGX A100 systems. Figure 8 shows the results of the NCCL all\_reduce\_perf test.

Figure 8) NCCL bandwidth test result.



## **FIO Bandwidth and IOPS Tests**

These tests are intended to measure the storage system performance using the synthetic I/O generator tool FIO. Two separate configurations were used, one optimized to deliver maximum bandwidth and the other optimized for IOPS. Each configuration was run with both 100% reads and 100% writes, and the files used by FIO were created as a separate step to isolate those activities from the actual test results. Here are the specific FIO configuration parameters for these tests:

- ioengine = posixaio
- direct = 1
- blocksize = 1024k for bandwidth test, 4k for IOPS test
- numjobs = 120 for bandwidth test, 180 for IOPS test
- iodepth = 32
- size = 4194304k

Figure 9 shows the results of the FIO bandwidth tests with one, two, and four DGX A100 systems.

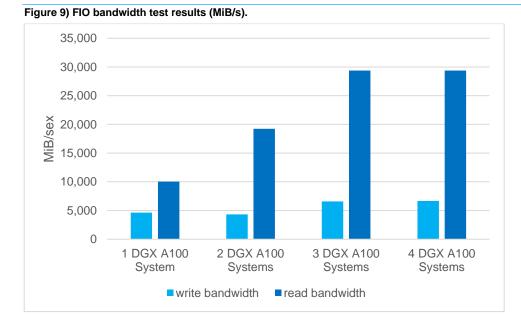


Figure 10 shows the results of the FIO IOPS test.



Figure 10) FIO IOPS test results (operations/sec).

## **Deep Learning Workload Validation**

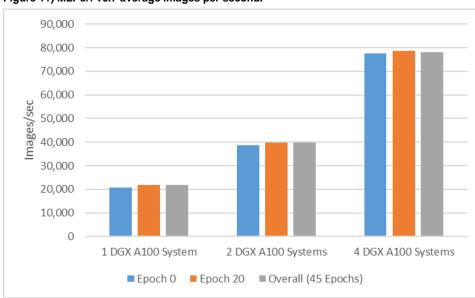
## MLPerf v0.7 ResNet-50

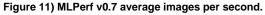
This reference architecture was tested using a MLPerf v0.7 benchmark to validate the operation of deep learning workloads on the deployed infrastructure. MLPerf is an industry-standard benchmark implementation of various neural networks for validating the performance of deep learning infrastructure. This test used the MXNet implementation of ResNet-50 along with the ImageNet dataset in IORecord format to validate model training performance. DALI was used to accelerate ingest and pre-processing of data, and Horovod was used to distribute the training across multiple DGX A100 systems. The results

presented maintain a consistent batch size per system of 408 images as the workload is scaled (weak scaling).

The base container image used for these tests is the 20.06 MXNet image from NGC. MLPerf benchmark tests are deliberately not optimized for any specific hardware implementation so overall system performance in these tests may be increased by tuning parameters such as concurrency.

Figure 11 shows the average images per second for the training run duration of 45 epochs. Note that performance of epoch 0 is roughly equivalent to the remaining epochs indicating that throughput is not the bottleneck for this training process. For this specific test, all target storage network interfaces were hosted on a single controller to validate the performance of the single controller and prepare for scaling to 8 nodes using the partner controller. Testing with 8 DGX A100 systems is underway and these results will be updated when testing is complete.





## **Solution Sizing Guidance**

This architecture is intended as a reference for customers and partners who would like to implement a deep learning infrastructure with NVIDIA DGX A100 systems and a NetApp AFF system.

As is demonstrated in this validation, the AFF A800 system easily supports the DL training workload generated by four DGX A100 systems. For larger deployments with higher storage performance requirements, additional AFF A800 systems can be added to the NetApp ONTAP cluster. ONTAP 9 supports up to 12 HA pairs (24 nodes) in a single cluster. With the FlexGroup technology validated in this solution, a 24-node cluster can provide over 20PB and up to 300GB/s throughput in a single volume. While the dataset used in this validation was relatively small, ONTAP 9 can scale to impressive capacity with linear performance scalability, because each HA pair delivers performance comparable to the level verified in this document.

Other NetApp storage systems such as the A400 offer lower performance and capacity options for smaller deployments at lower cost points. Based on the results of this testing, an A400 can support one or two DGX A100 systems with the workloads that were tested. Because ONTAP 9 supports mixed-model clusters, customers can start with a smaller initial footprint and add more or larger storage systems to the cluster as capacity and performance requirements grow.

# Conclusion

The DGX A100 system is a next-generation deep learning platform that requires equally advanced storage and data management capabilities. By combining DGX A100 with NetApp AFF systems, this verified architecture can be implemented at almost any scale, from a single DGX A100 paired with an AFF A400 storage system up to potentially 48 DGX A100 systems on a 12-node AFF A800 cluster. Combined with the superior cloud integration and software-defined capabilities of NetApp ONTAP, AFF enables a full range of data pipelines that spans the edge, the core, and the cloud for successful DL projects.

# Acknowledgments

The authors gratefully acknowledge the contributions that were made to this technical report by our esteemed colleagues from NVIDIA and NetApp. Our sincere appreciation and thanks go to all the individuals who provided insight and expertise that greatly assisted in the research for this paper.

# Where to Find Additional Information

To learn more about the information that is described in this document, review the following resources:

## NetApp AFF Systems

- AFF datasheet <u>https://www.netapp.com/us/media/ds-3582.pdf</u>
- NetApp Flash Advantage for AFF <u>https://www.netapp.com/us/media/ds-3733.pdf</u>
- ONTAP 9.x documentation
  <u>http://mysupport.netapp.com/documentation/productlibrary/index.html?productID=62286</u>
- NetApp FlexGroup technical report <u>https://www.netapp.com/us/media/tr-4557.pdf</u>

## **NetApp Interoperability Matrix**

 NetApp Interoperability Matrix Tool <u>http://support.netapp.com/matrix</u>

## NetApp Trident

- <u>https://netapp.io/persistent-storage-provisioner-for-kubernetes/</u>
- <u>https://netapp-trident.readthedocs.io/en/stable-v19.04/kubernetes/index.html</u>
- <u>https://github.com/NetApp/trident</u>

## NVIDIA DGX A100 Systems

- NVIDIA DGX A100 systems <u>https://www.nvidia.com/en-us/data-center/dgx-a100/</u>
- NVIDIA A100 Tensor core GPU <u>https://www.nvidia.com/en-us/data-center/a100/</u>
- NVIDIA GPU Cloud
  <u>https://www.nvidia.com/en-us/gpu-cloud/</u>

## **NVIDIA Mellanox Networking**

- NVIDIA Mellanox Spectrum SN3000 series switches <u>https://www.mellanox.com/products/ethernet-switches/sn3000</u>
- NVIDIA Mellanox Quantum QM8700 series IB switches <u>https://www.mellanox.com/products/infiniband-switches/QM8700</u>

## **Machine Learning Frameworks**

- TensorFlow: An Open-Source Machine Learning Framework for Everyone
  <u>https://www.tensorflow.org/</u>
- Horovod: Uber's Open-Source Distributed Deep Learning Framework for TensorFlow
  <a href="https://eng.uber.com/horovod/">https://eng.uber.com/horovod/</a>
- Enabling GPUs in the Container Runtime Ecosystem <u>https://devblogs.nvidia.com/gpu-containers-runtime/</u>

## **Dataset and Benchmarks**

- ImageNet
  <u>http://www.image-net.org/</u>
- MLPerf training and inference benchmarks
  <u>https://mlperf.org/</u>

# **Version History**

Version	Date	Document Version History
Version 1.0	October 2020	Initial release.

Refer to the <u>Interoperability Matrix Tool (IMT)</u> on the NetApp Support site to validate that the exact product and feature versions described in this document are supported for your specific environment. The NetApp IMT defines the product components and versions that can be used to construct configurations that are supported by NetApp. Specific results depend on each customer's installation in accordance with published specifications.

#### **Copyright Information**

Copyright © 2020 NetApp, Inc. All Rights Reserved. Printed in the U.S. No part of this document covered by copyright may be reproduced in any form or by any means—graphic, electronic, or mechanical, including photocopying, recording, taping, or storage in an electronic retrieval system—without prior written permission of the copyright owner.

Software derived from copyrighted NetApp material is subject to the following license and disclaimer:

THIS SOFTWARE IS PROVIDED BY NETAPP "AS IS" AND WITHOUT ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE, WHICH ARE HEREBY DISCLAIMED. IN NO EVENT SHALL NETAPP BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.

NetApp reserves the right to change any products described herein at any time, and without notice. NetApp assumes no responsibility or liability arising from the use of products described herein, except as expressly agreed to in writing by NetApp. The use or purchase of this product does not convey a license under any patent rights, trademark rights, or any other intellectual property rights of NetApp.

The product described in this manual may be protected by one or more U.S. patents, foreign patents, or pending applications.

Data contained herein pertains to a commercial item (as defined in FAR 2.101) and is proprietary to NetApp, Inc. The U.S. Government has a non-exclusive, non-transferrable, non-sublicensable, worldwide, limited irrevocable license to use the Data only in connection with and in support of the U.S. Government contract under which the Data was delivered. Except as provided herein, the Data may not be used, disclosed, reproduced, modified, performed, or displayed without the prior written approval of NetApp, Inc. United States Government license rights for the Department of Defense are limited to those rights identified in DFARS clause 252.227-7015(b).

#### **Trademark Information**

NETAPP, the NETAPP logo, and the marks listed at <u>http://www.netapp.com/TM</u> are trademarks of NetApp, Inc. Other company and product names may be trademarks of their respective owners.

NVA-1151-DESIGN-0920

